

Fig. 1

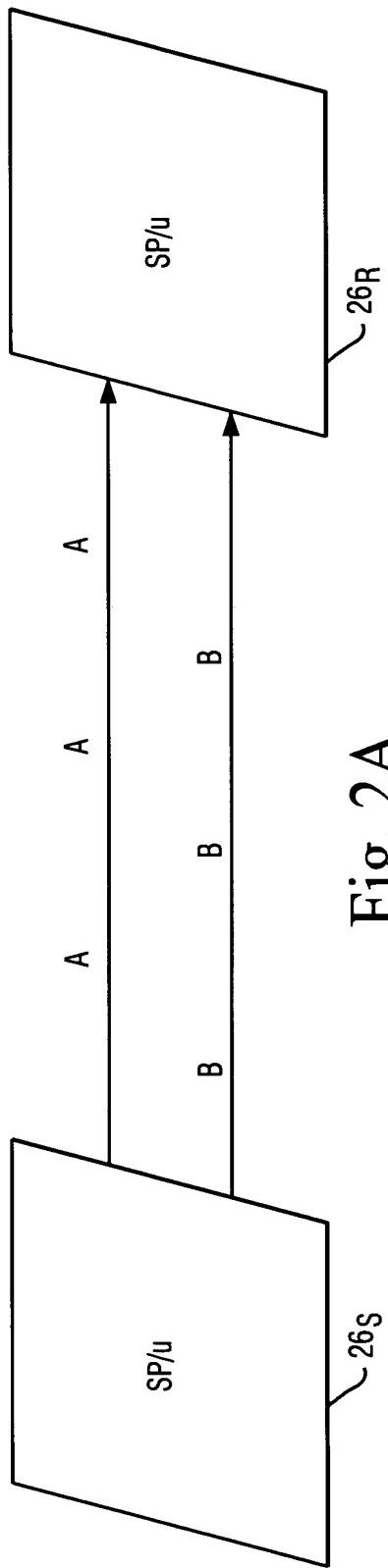


Fig. 2A

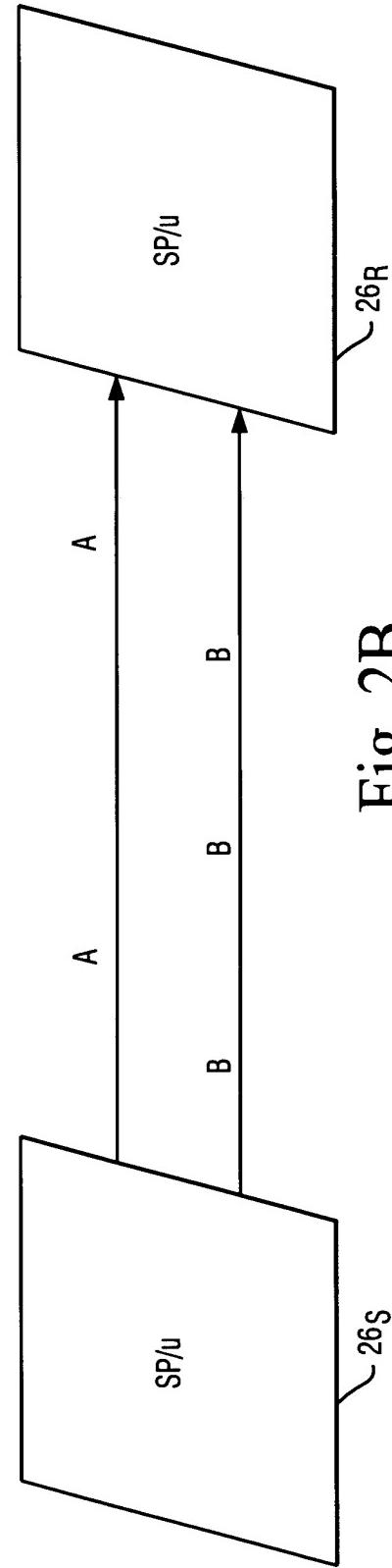


Fig. 2B

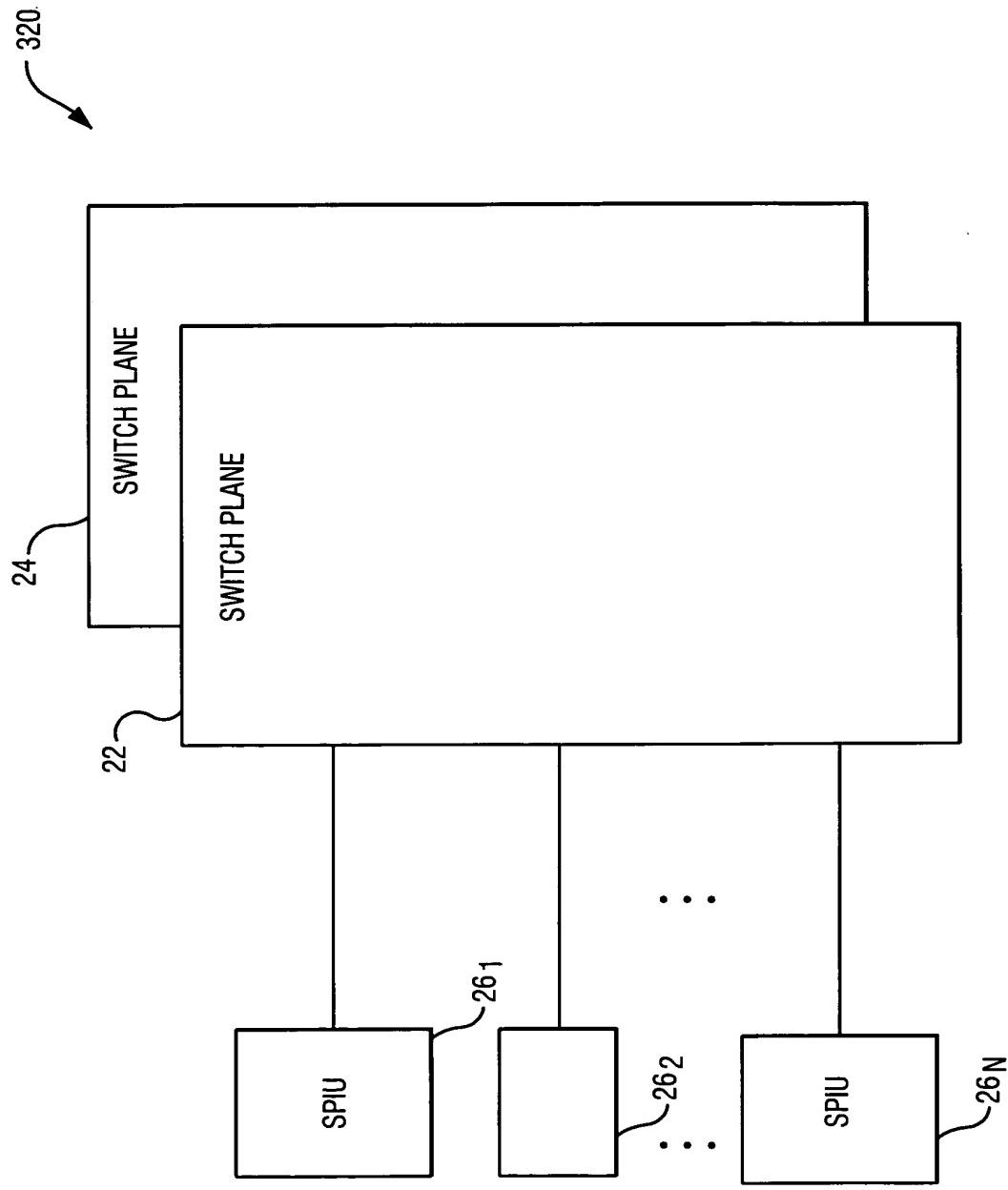


Fig. 3

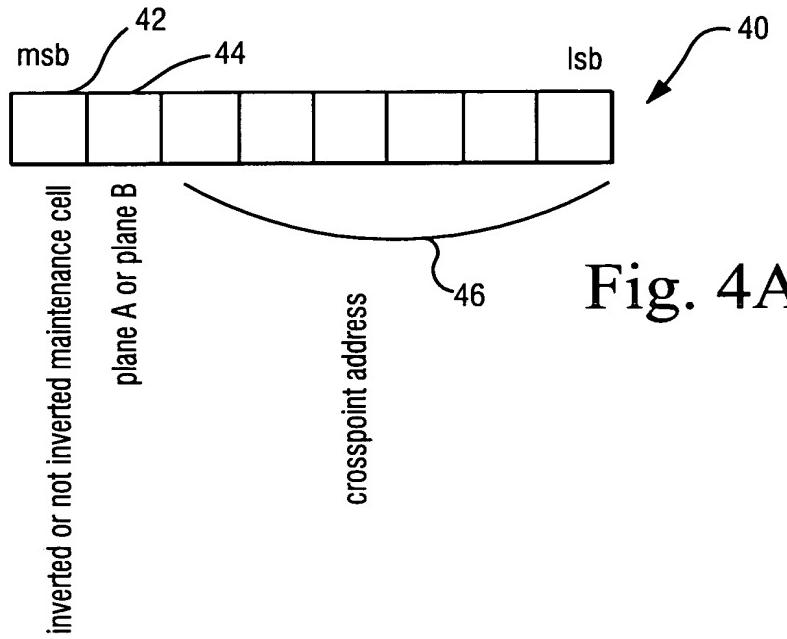


Fig. 4A

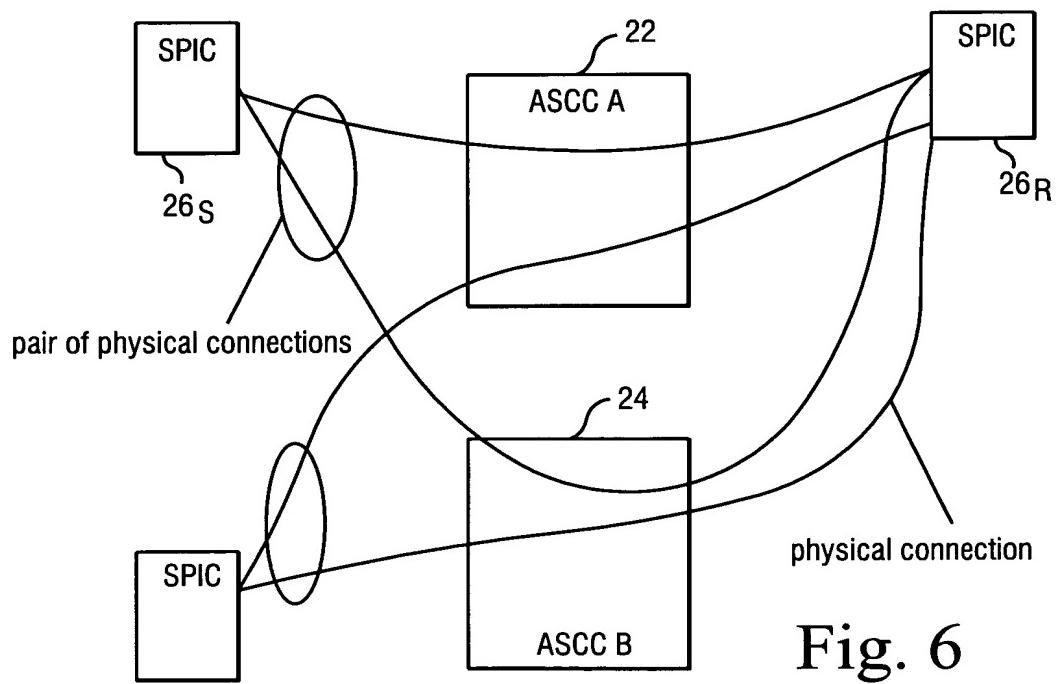


Fig. 6

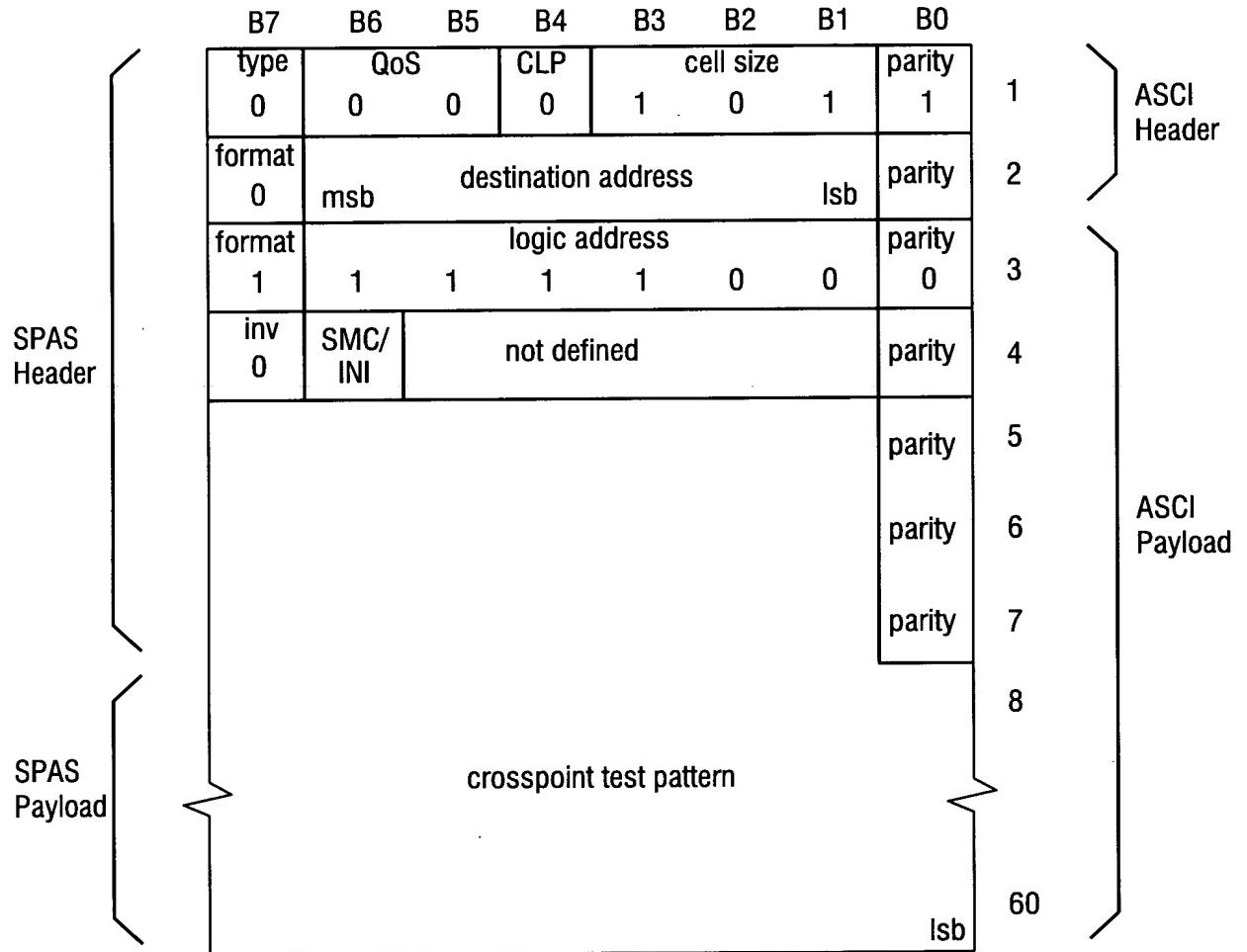


Fig. 4B(1) Initialisation Cell

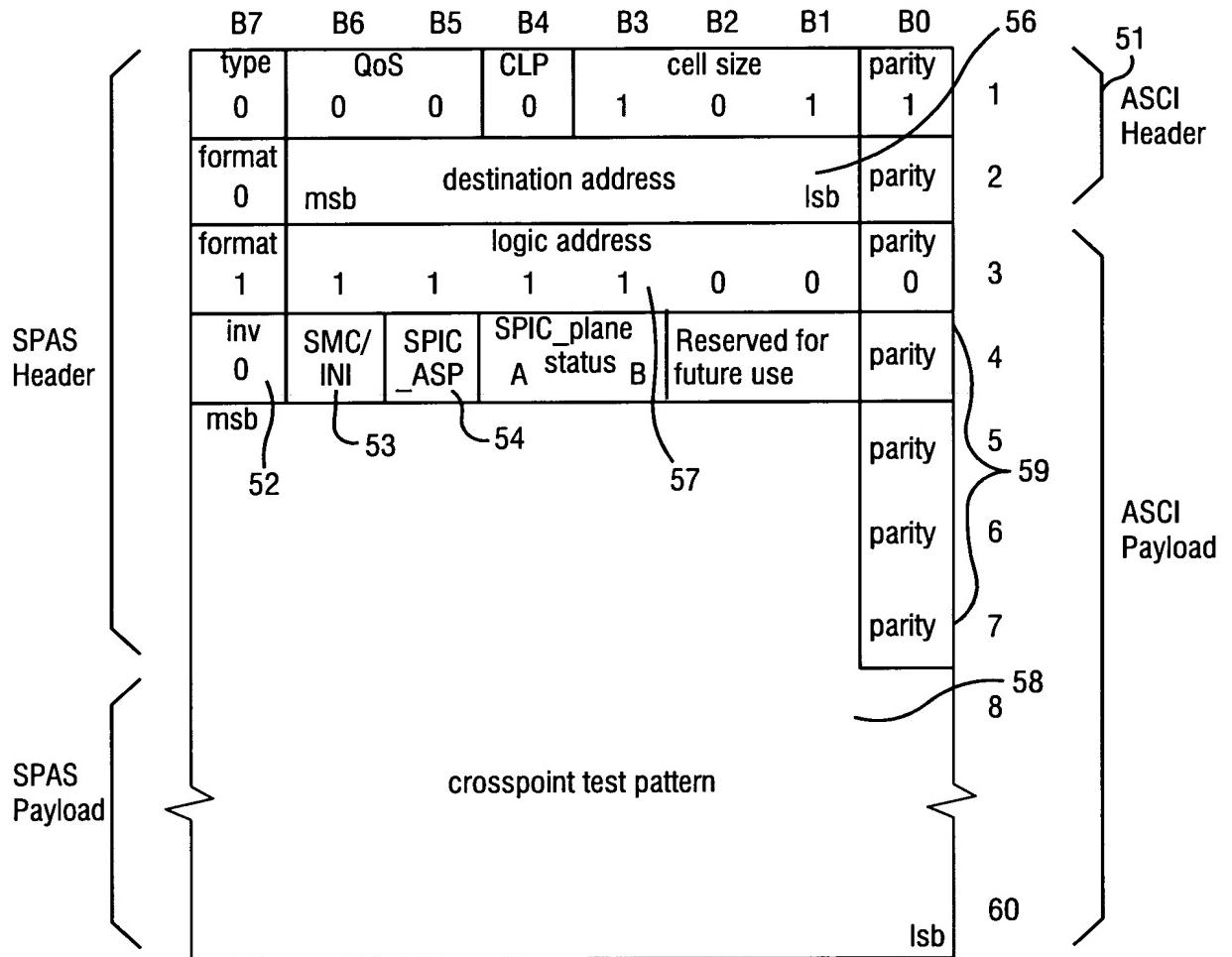


Fig. 4B(2) Initialisation Cell



Fig. 5A

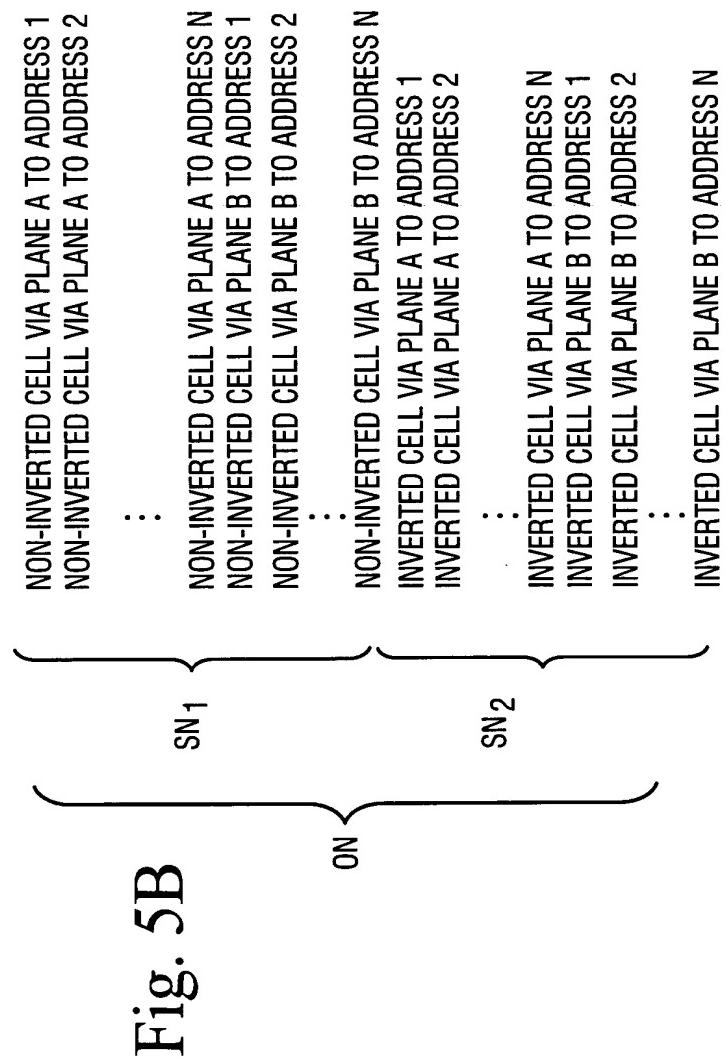


Fig. 5B

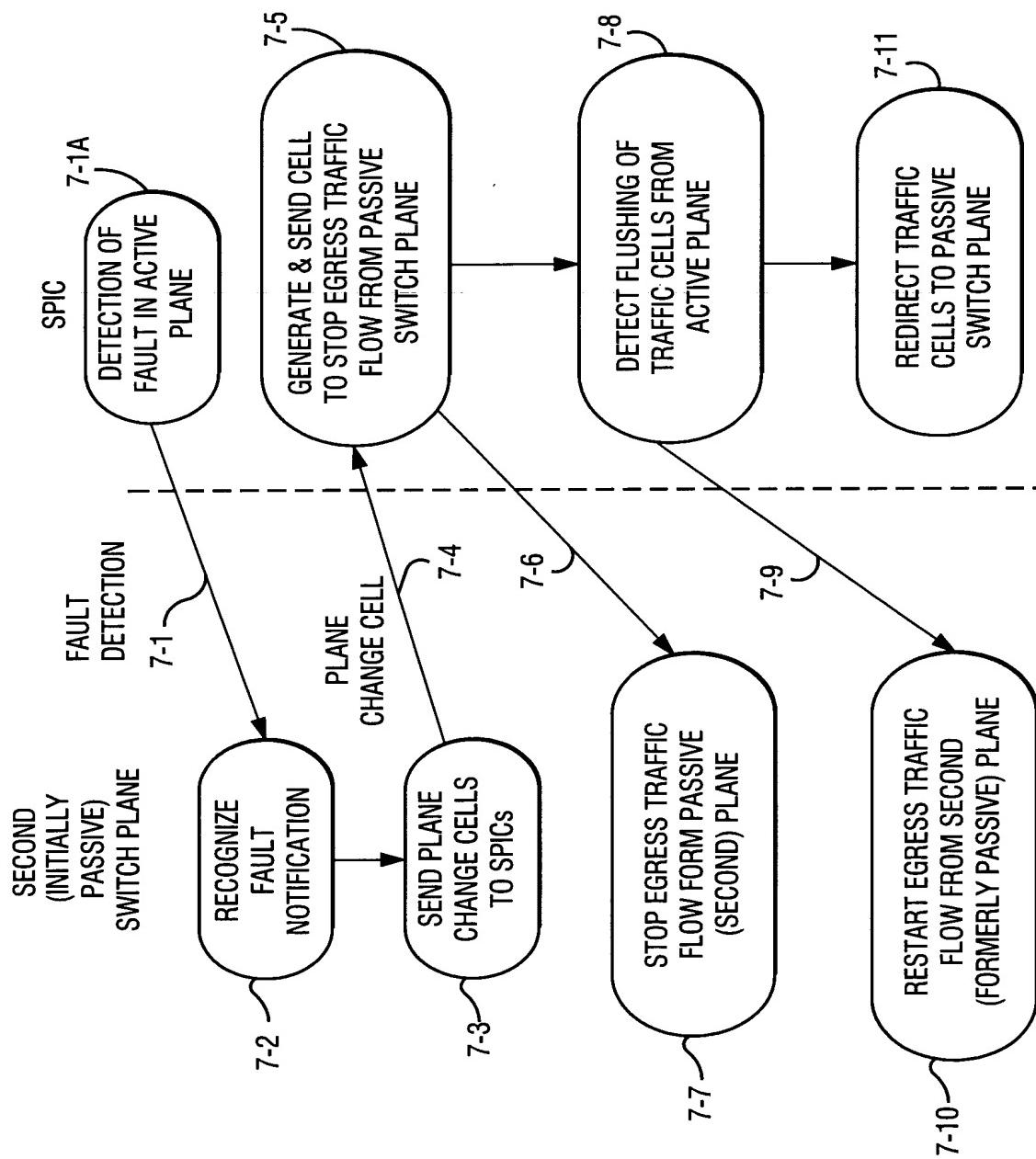


Fig. 7

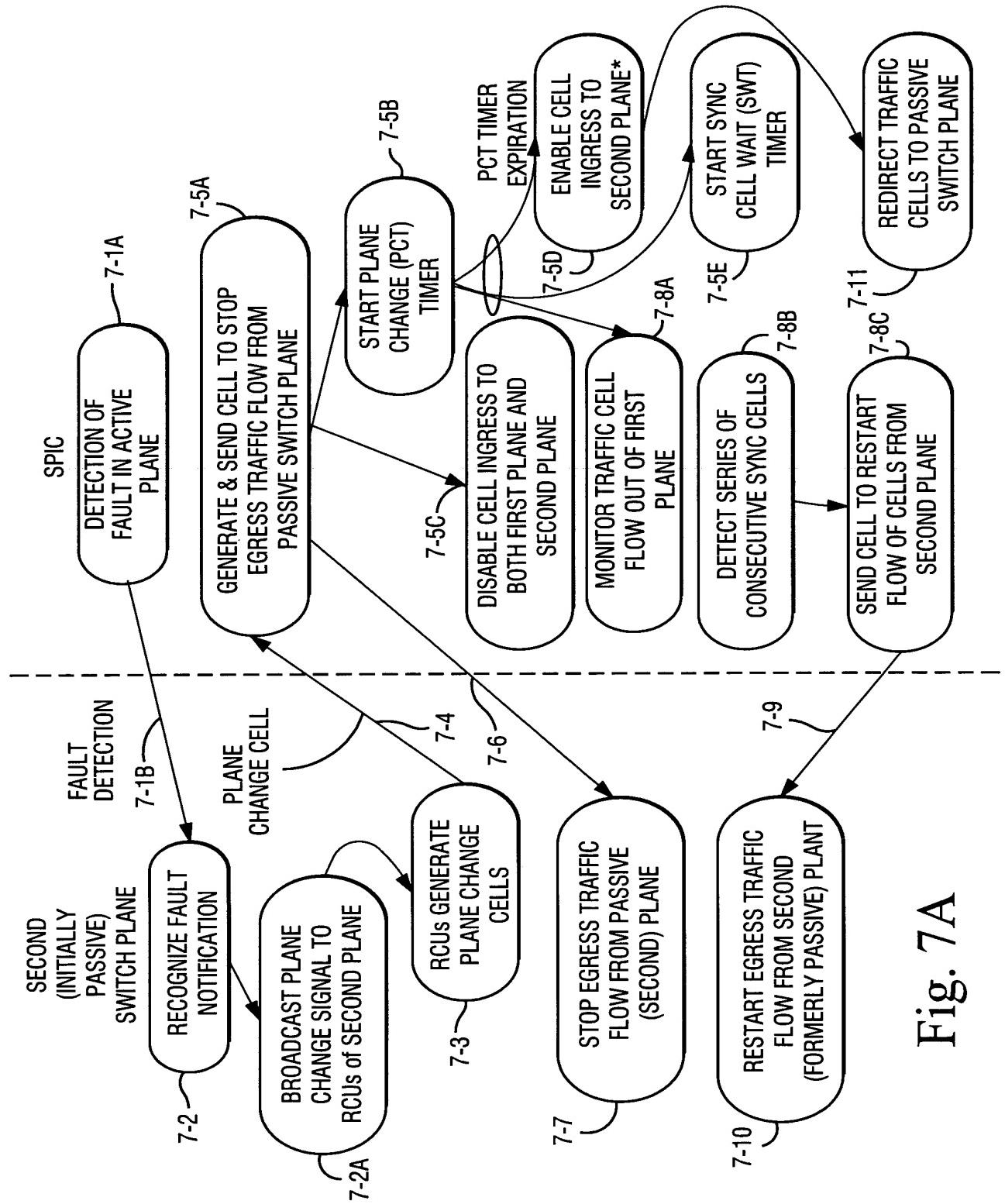


Fig. 7A

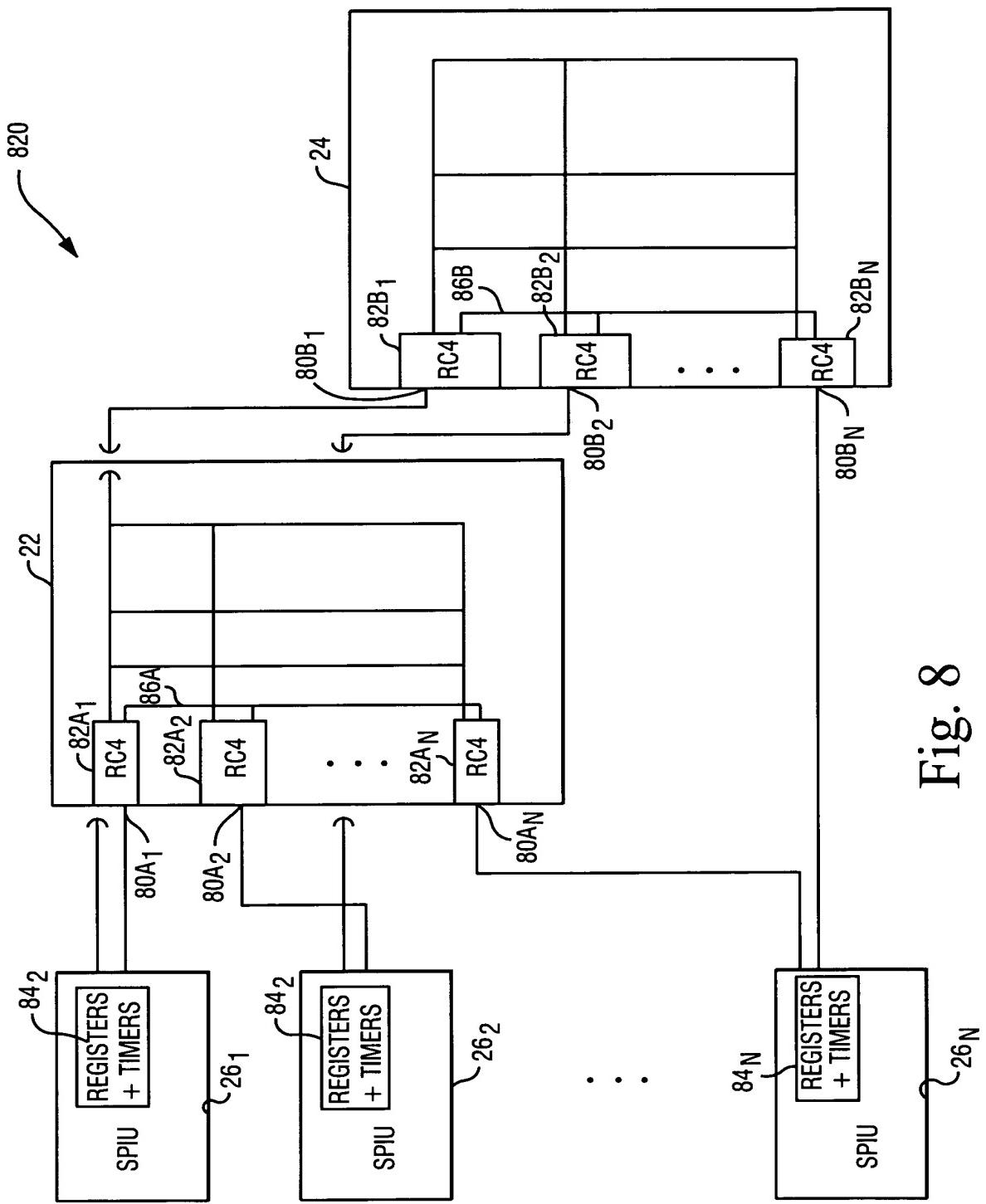


Fig. 8

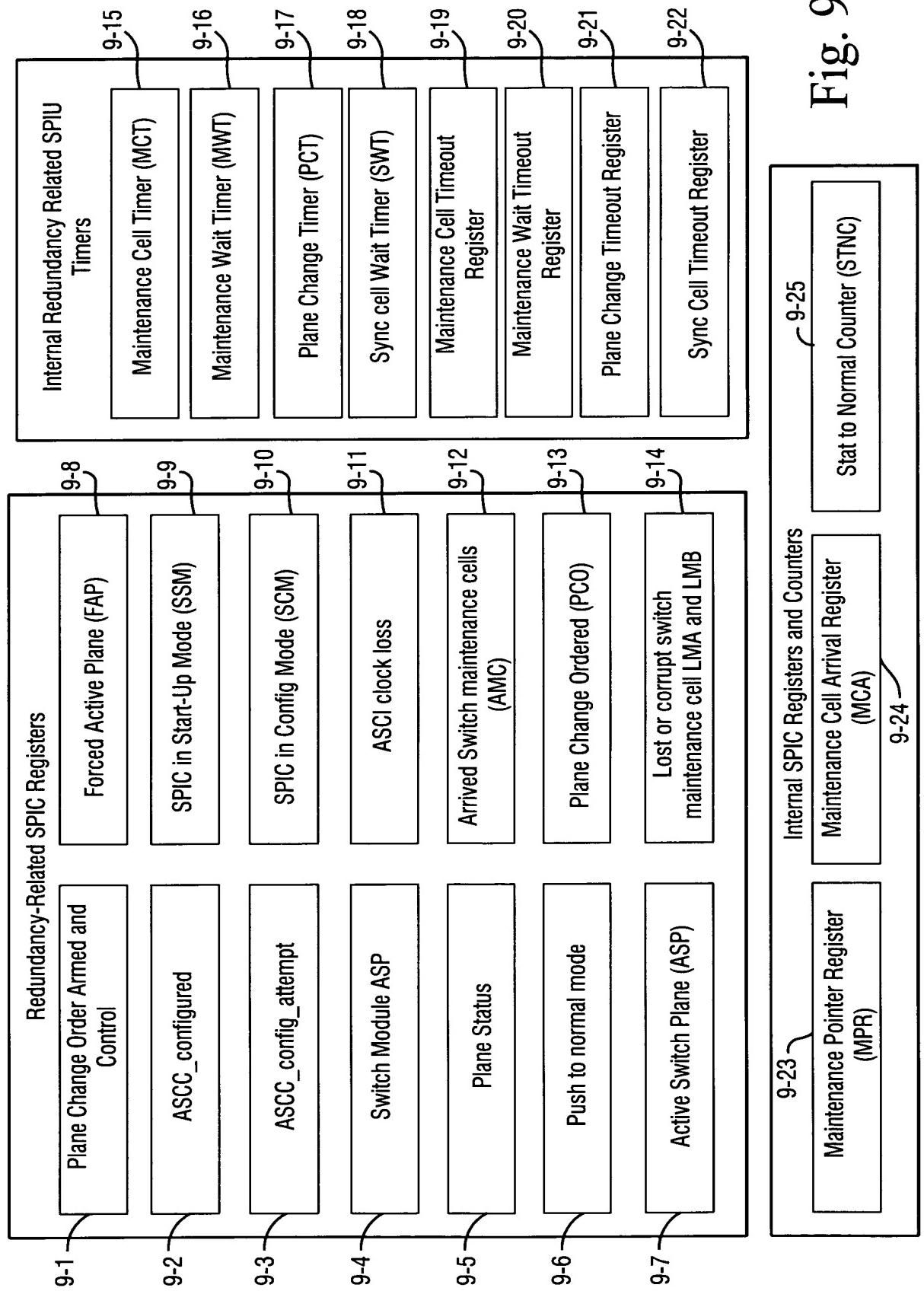


Fig. 9

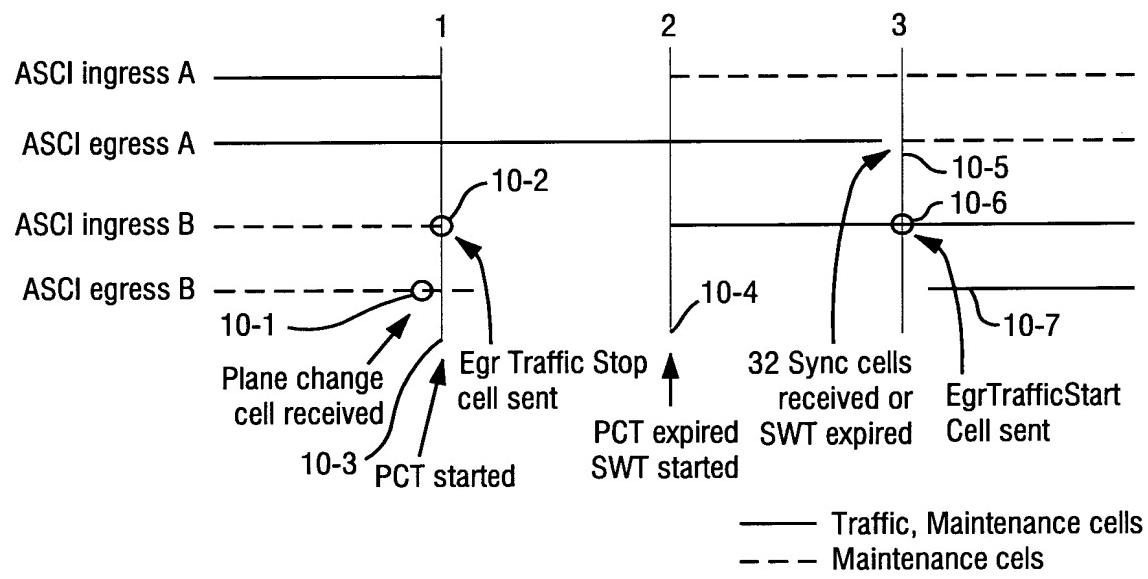


Fig. 10

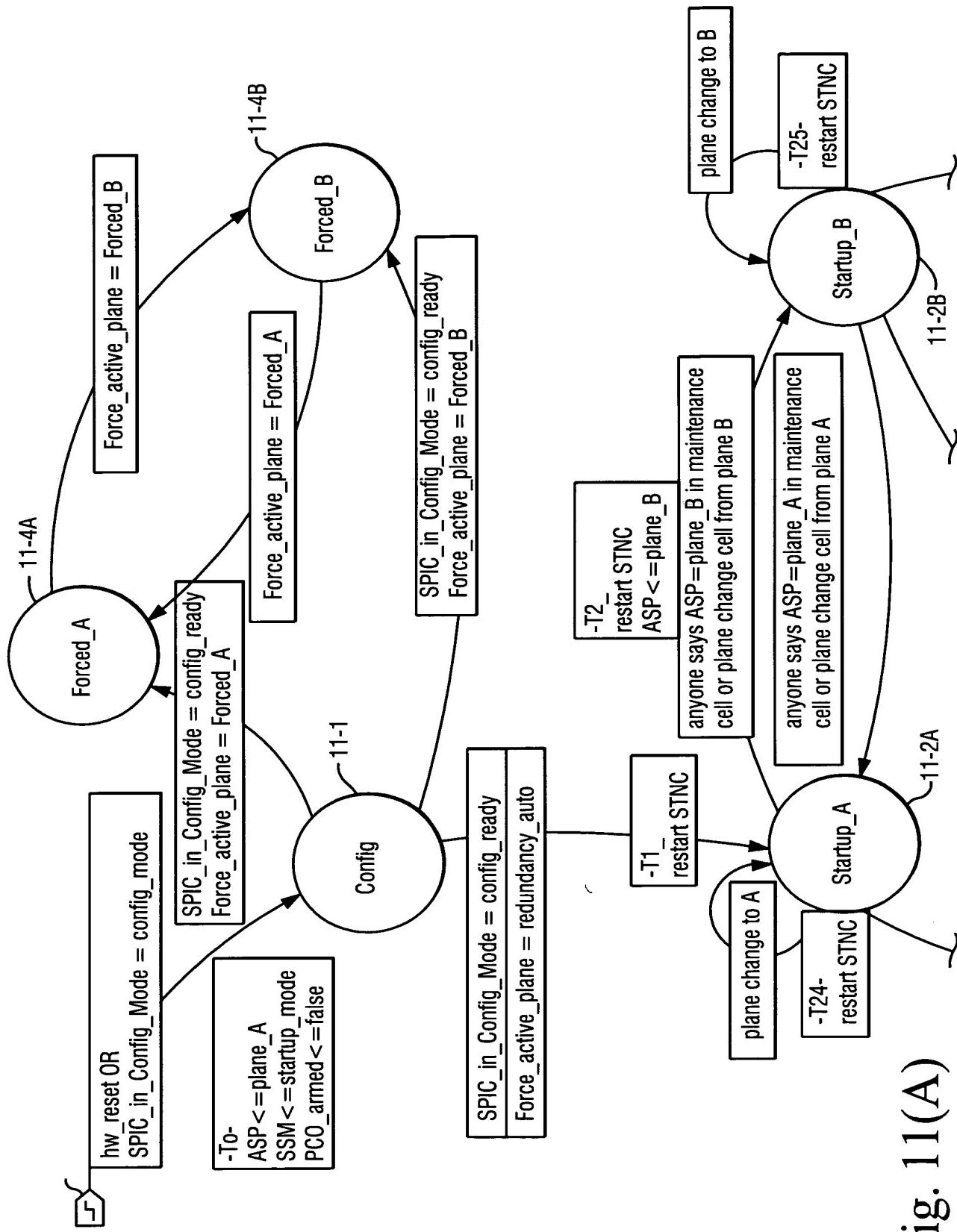
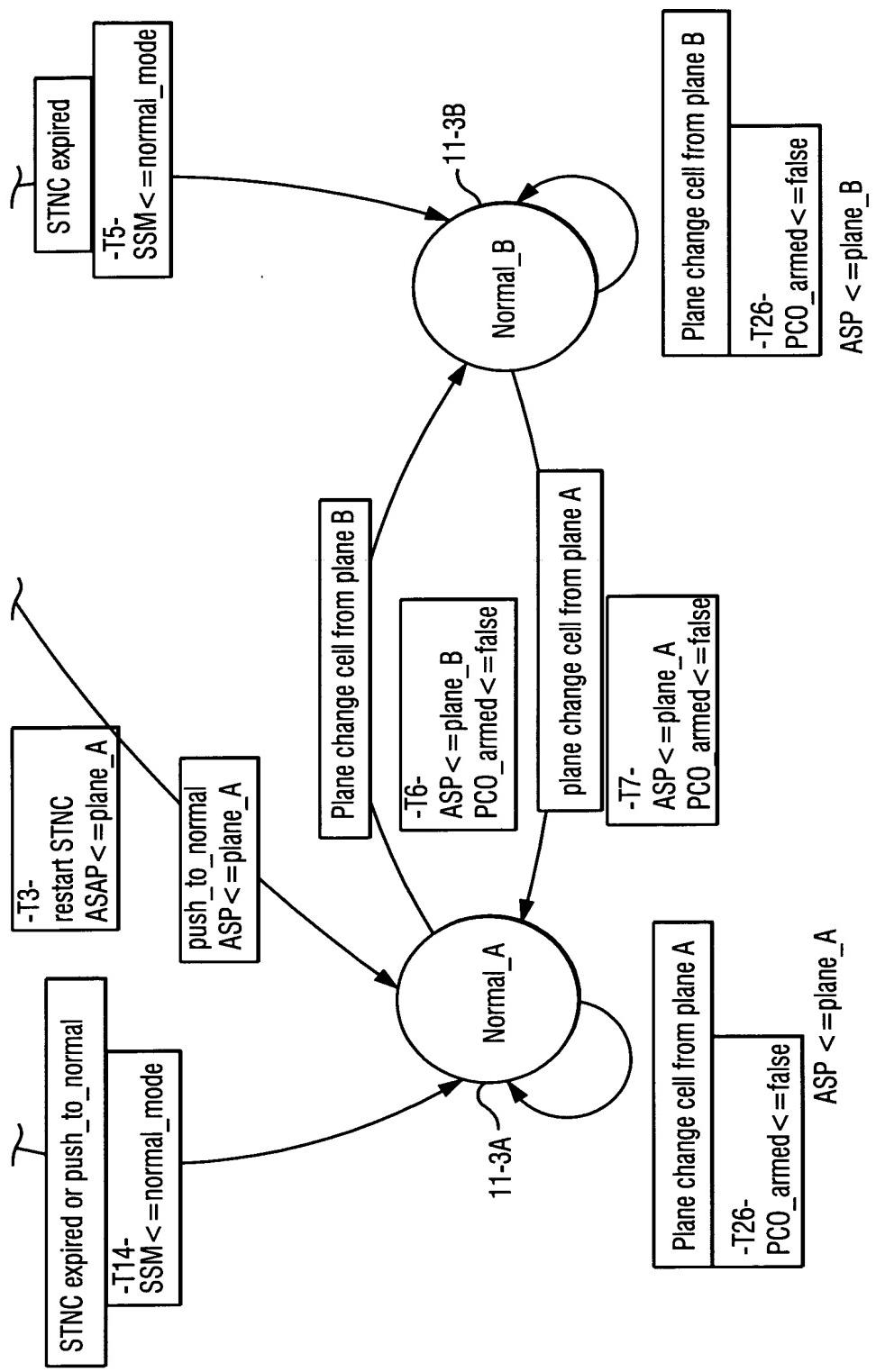


Fig. 11(A)



ASP	= Active switch plane
SSM	= SPIC in start-up mode
PCO	= Plain change order armed
STNC	= Start to normal counter

Fig. 11(B)

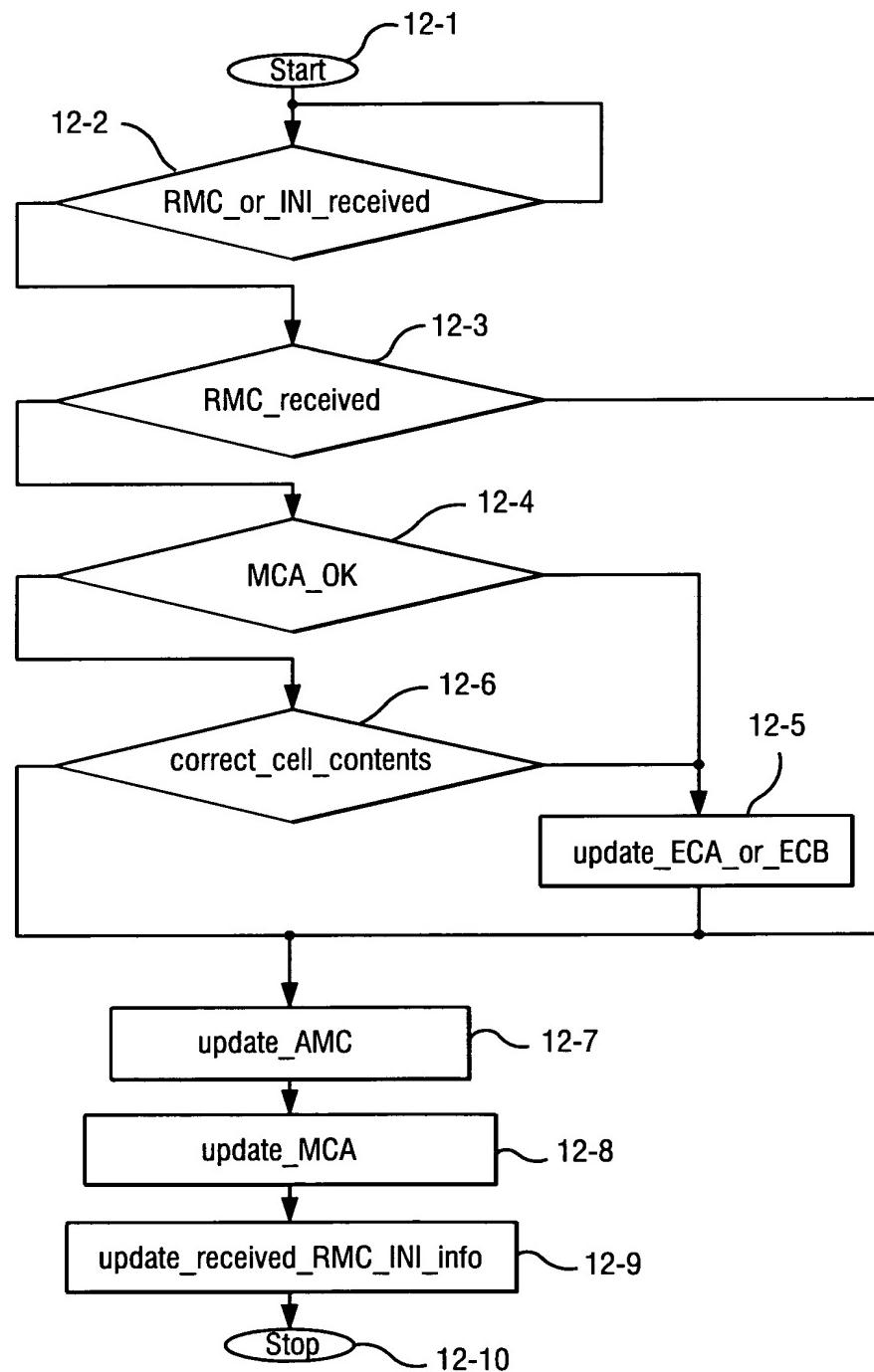


Fig. 12

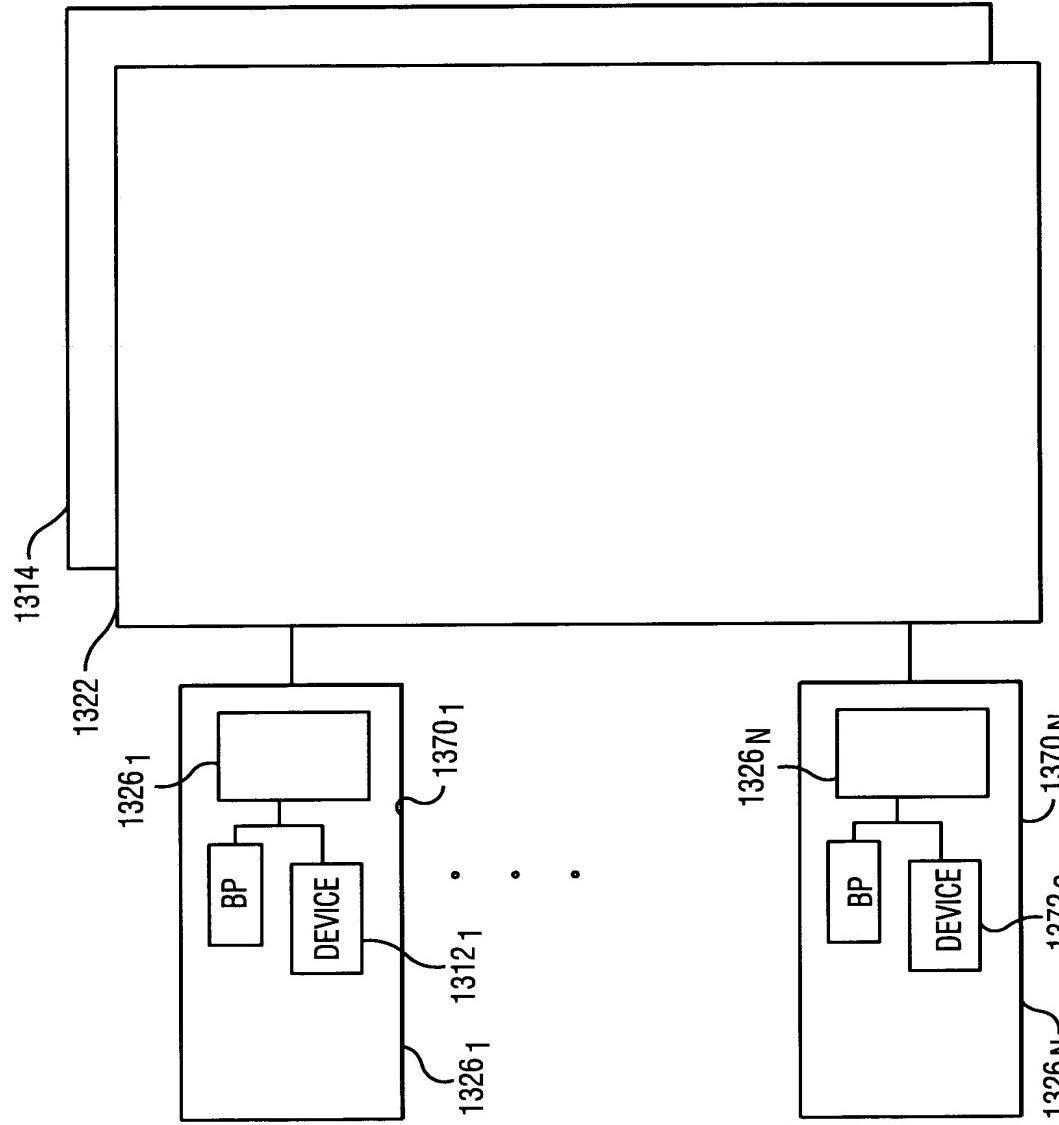


Fig. 13